

Job Description: Senior image sensor design engineer

Within the design team, you are one of the technical leaders for the development of an image sensor. You are responsible for the development of an image sensor, according to its specifications and schedule. You assess feasibility by circuit simulations and verifications on the critical items. You manage a small team to develop the layout of the chip, and contribute yourself to this. Responsibilities include delivering a fully designed chip on-time, reporting and interaction with customers about the development, specifying the package and glass design, and dealing with subcontractors for wafer manufacturing. You work with state-of-the-art IC design tools from Cadence and Mentor Graphics for the design and verification of the chips. You also contribute to the functional testing and characterisation of your image sensor designs.

Minimum requirements

- Master, M.Sc. or Ph.D. in Electronics Engineering
- 5+ years of relevant experience in analog or mixed-signal IC development
- Demonstrated experience as a design lead for complex analog or mixed-signal chips.
- Good background in solid-state electronic devices
- Excellent analytical and problem solving skills
- Fluent written and verbal communication skills in English
- Team player with strong technical leadership
- Accurate, self-critical, hands-on and self-managed personality

Additional desired qualifications

- 5+ years of experience in image sensor design
- 10+ years of relevant experience in analog or mixed-signal IC development
- Experience with analog integrated circuit design, digital design, chip layout or verification
- Experience with management of IC development projects
- Understanding of silicon device processing
- Knowledge about optics or solid-state physics

Adaptability, continuous learning, decision-making and management of multiple tasks are also desired

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