

Job Description: Test Engineer (Hardware)

As a test engineer you will create test solutions for our in-house developed CMOS image sensors. These test systems will be used during characterization, qualification and production testing. You will cooperate with our designers to come up with the most suitable systems.

Your tasks will be (amongst others): Schematic design and PCB layout, writing firmware for FPGA's in the test system and creating lab software to enable debugging and test of the image sensor. This development will run during sensor design in order to be ready with the test system when first silicon is ready

Profile

- Bachelor or Master in Electronics
- Experienced with FPGA's and PCB design
- Knowledge of VHDL, Simulation and implementation tools
- Veratile, all-round skillset
- Languages: English (Fluent) or Dutch (Fluent)

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